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Date October 15, 2004	Client/Matter Number 100806-00221
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U.S. Patent Application No.: 10/626,431

METHOD AND CIRCUIT FOR CHARGING A SIGNAL VOLTAGE THROUGH A SEMICONDUCTOR MEMORY DEVICE

Our Reference No.: NECG 20.529 (100806-00221)

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COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor

Tomio Okuda

Title

METHOD AND CIRCUIT FOR CHARGING A SIGNAL VOLTAGE THROUGH A SEMICONDUCTOR MEMORY

DEVICE

Serial No.

10/626,431

Confirmation No.

9823

Filed

07/23/2003

Examiner

David Lam

Group Art Unit

2818

Mail Stop: Issue Fee Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Comments on Statement of Reasons for Allowance

Sir:

The referenced application was allowed on, July 29, 2004. The issue fee due on October 29, 2004 is being paid concurrently herewith.

Applicant hereby acknowledges the Examiner's Reasons for Allowance. Applicant respectfully notes that there may be additional reasons for allowance that have not been specifically cited, and which may apply to various of the allowed claims, in addition to or instead of the cited reasons. Applicant respectfully suggests that notwithstanding the Examiner's

Reasons for Allowance, it is believed that each of the allowed claims is patentable in its own right and/or for other reasons raised during the prosecution and/or explained in the specification of this application.

To the extent that any statements regarding patentability of any claims allowed by the Examiner made by the Applicant or the Examiner in any document filed in this application are inconsistent with or not included in the Examiner's Reasons for Allowance, they are incorporated by reference herein.

Any fee due with this paper may be charged on Deposit Account No. 50-1290.

Respectfully submitted,

Reg. No. 30,659

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Docket No.: NECG 20.529 (100806-00221)

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